

Prior Art

Figure 1

FIG. 2 is a block diagram of a system 200, according to one embodiment of the present invention.

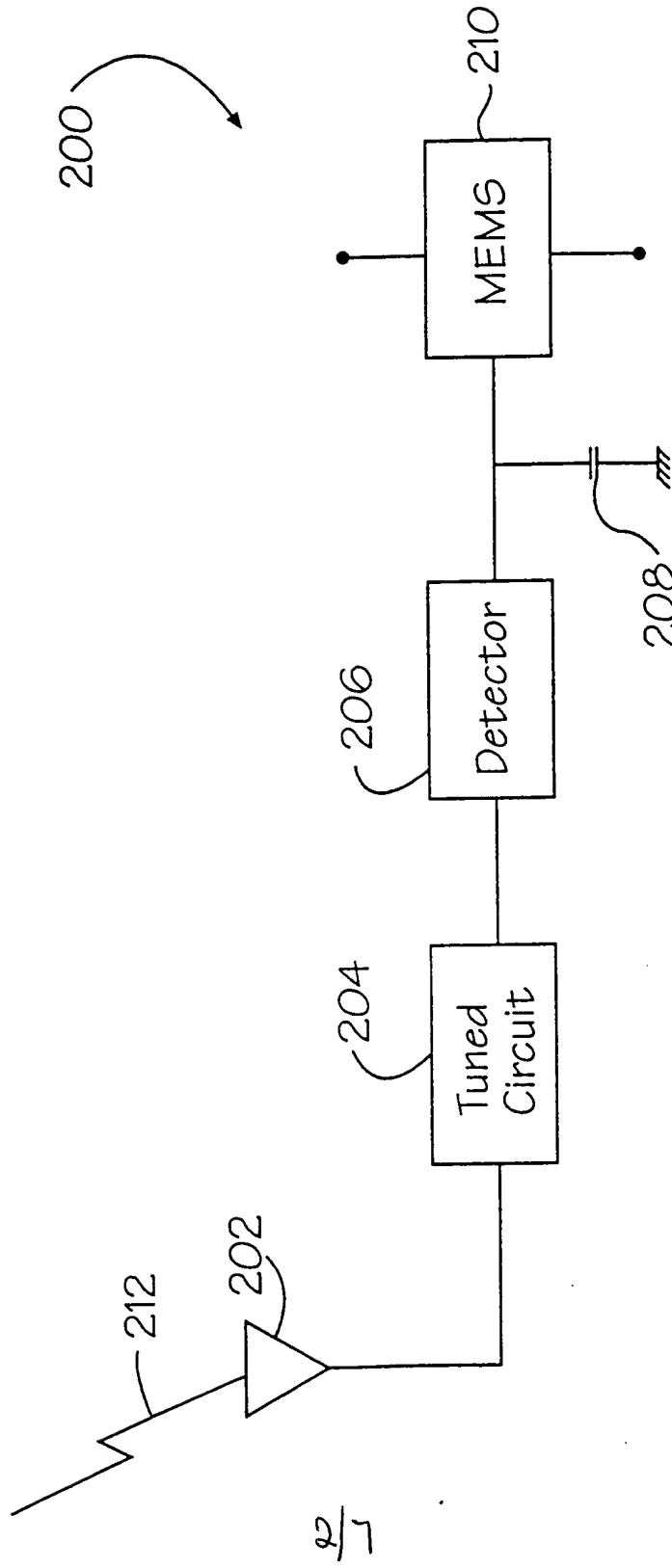


Figure 2

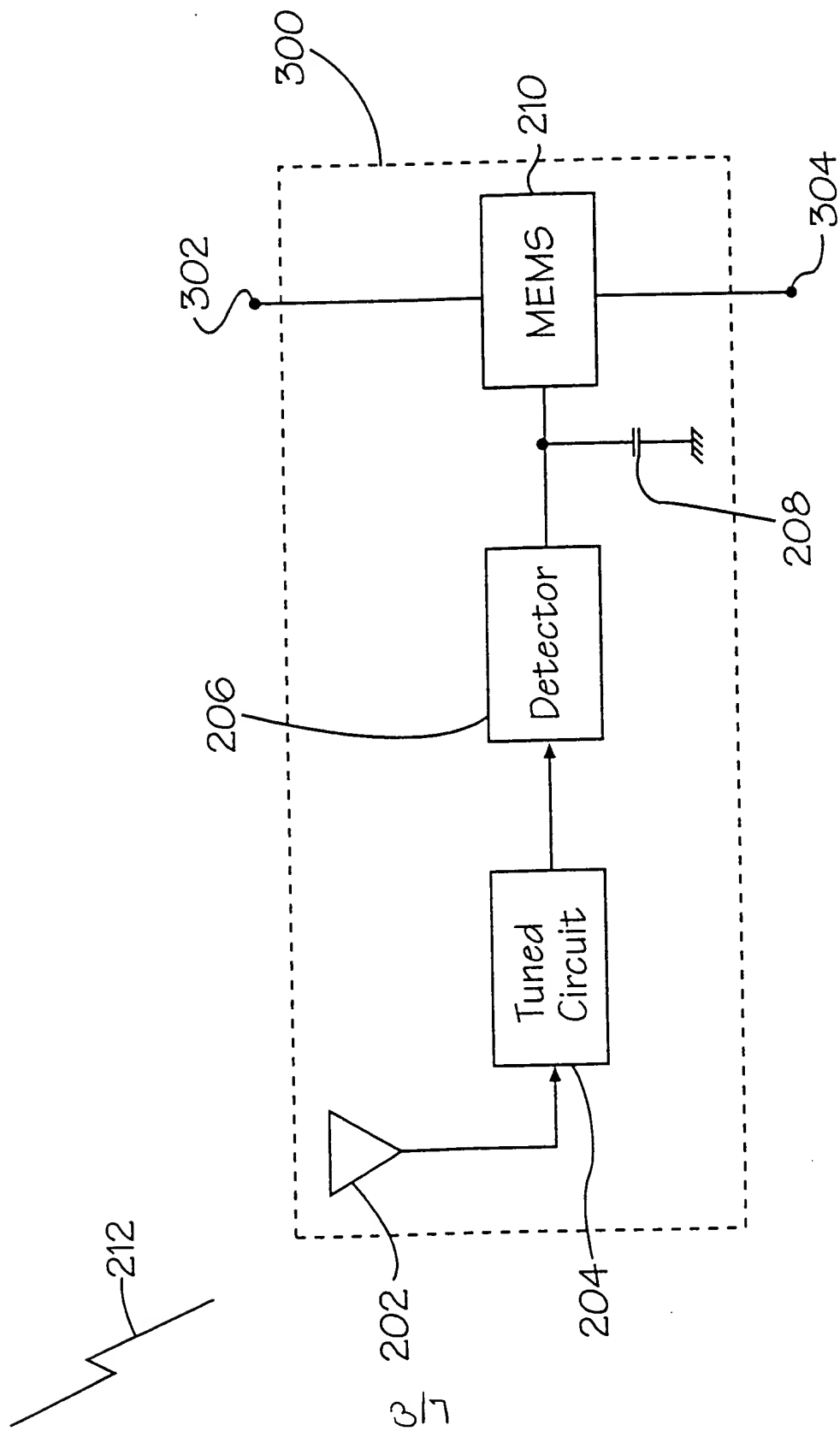


Figure 3

FIG. 4 is a schematic diagram of a system 400.

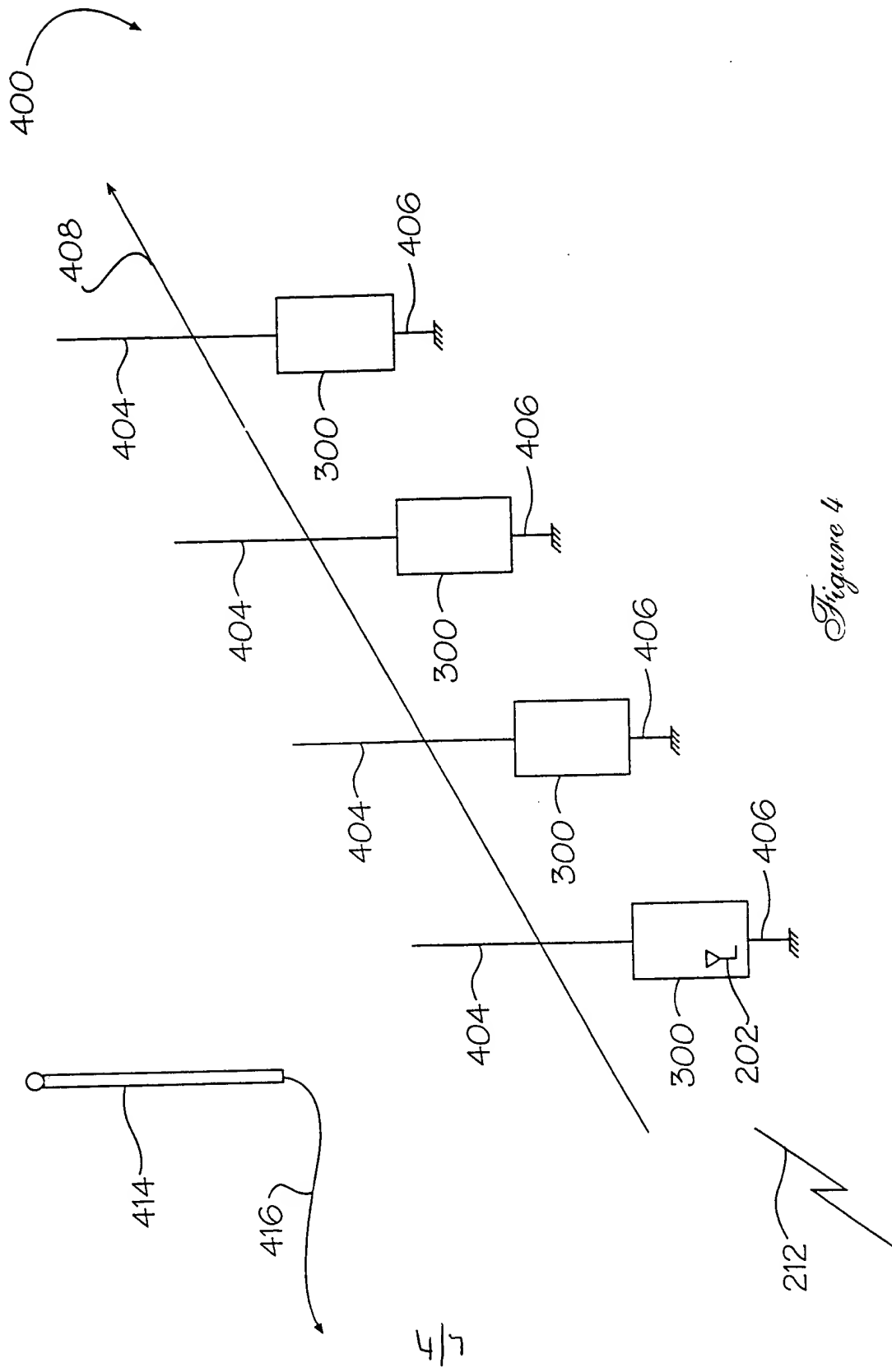


Figure 4

Figure 5

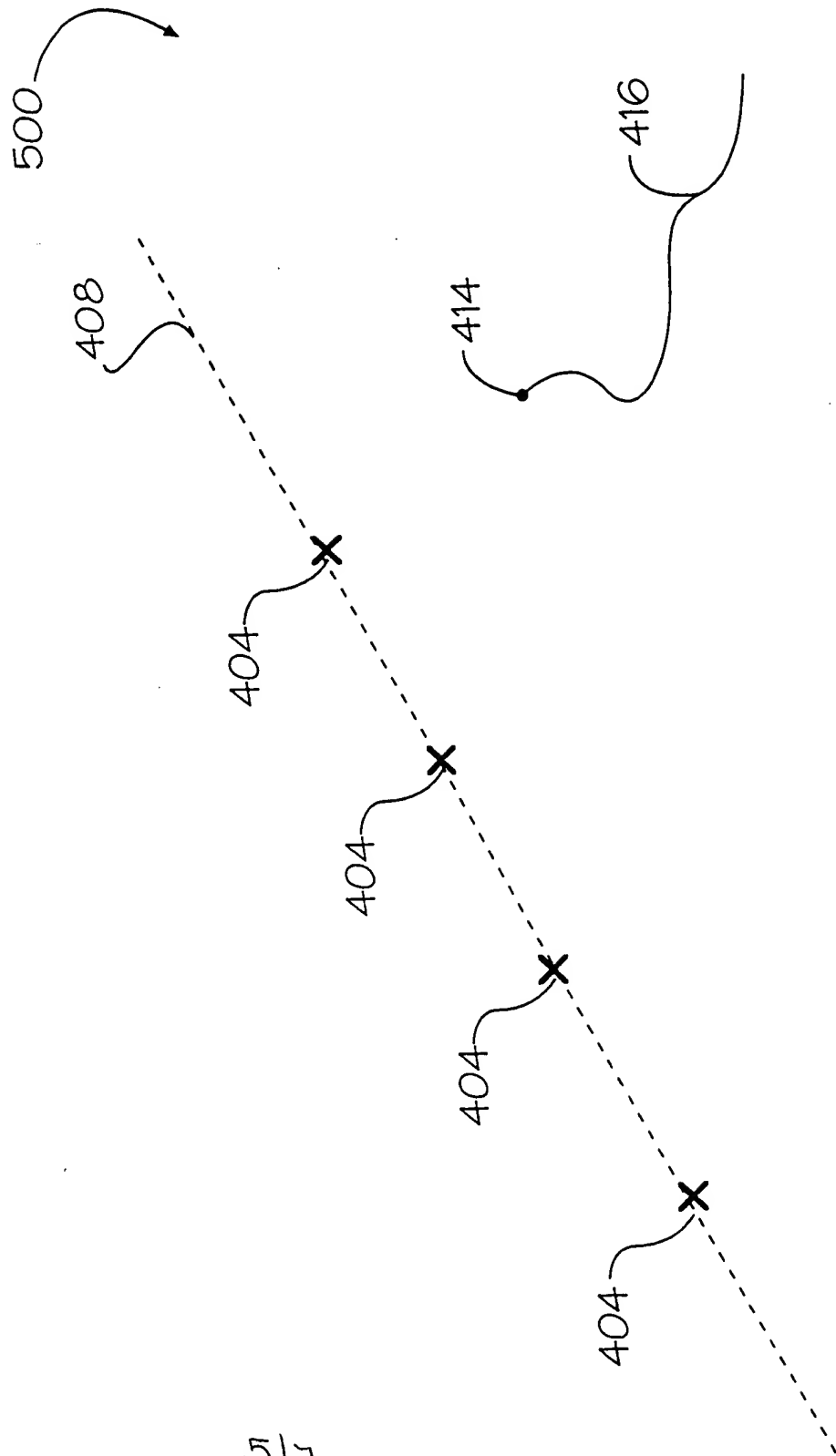


Figure 5

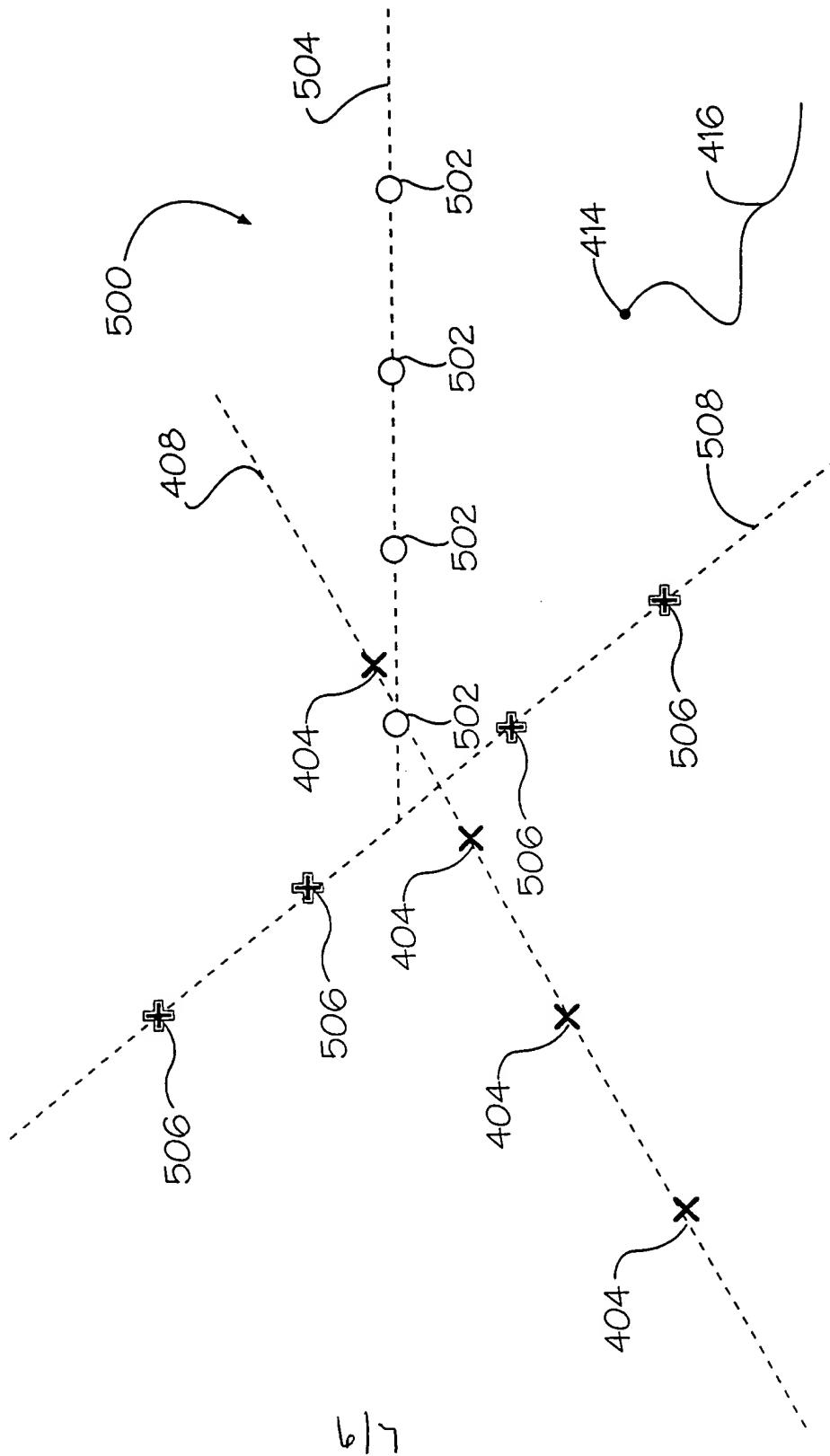


Figure 6

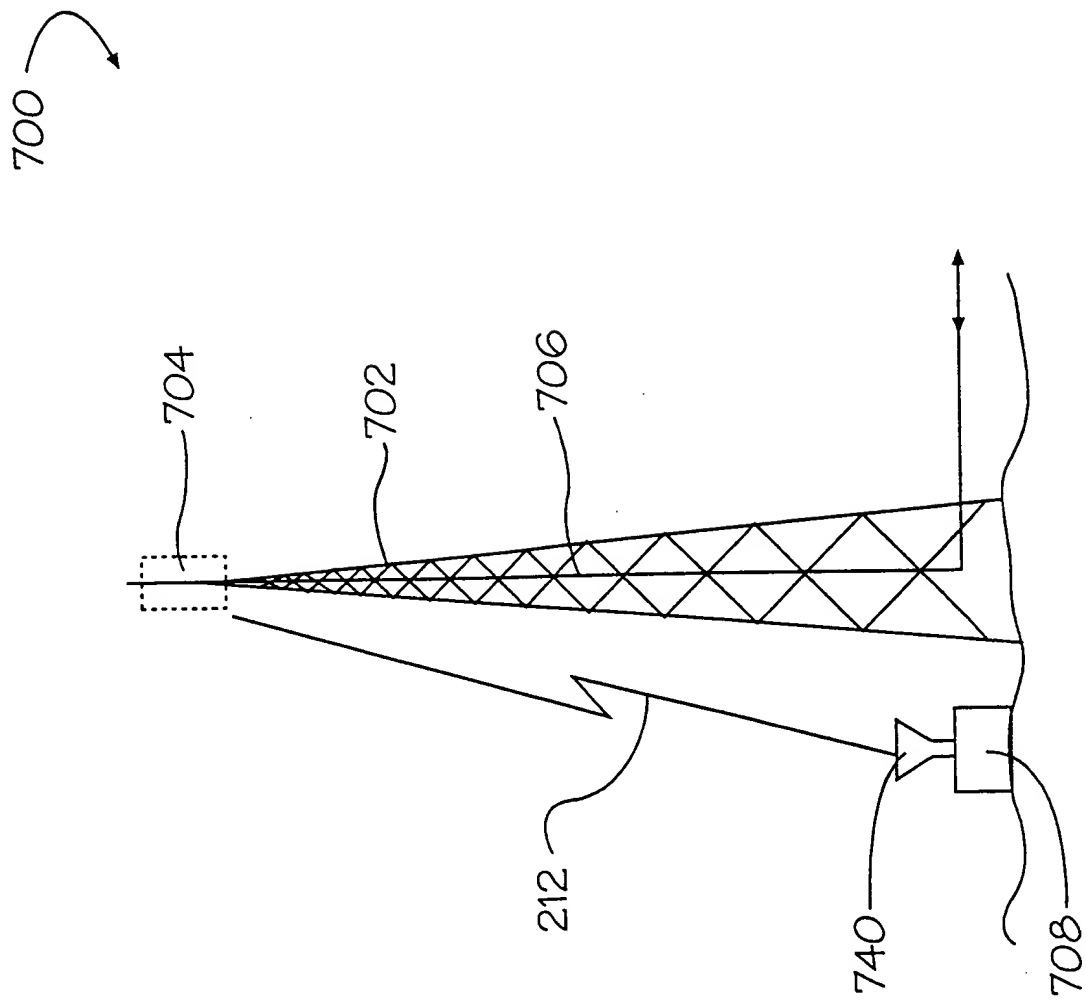


Figure 7